

IN THE UNITED STATES PATENT OFFICE

Applicant: Eric J. Crabill
Assignee: Xilinx, Inc.
Title: "Configurable Processor System"
Serial No.: Not Yet Known File Date: 03-19-04
Examiner: Not Yet Known Art Unit: Not Yet Known

Continuation of
Serial No.: 09/803,524 File Date: 03-08-01
Docket No.: X-841-1C US

Mail Stop PATENT APPLICATION
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INFORMATION DISCLOSURE STATEMENT

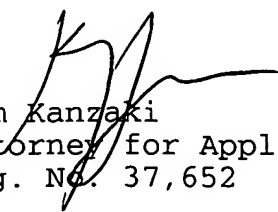
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant(s) bring(s) to the attention of the Examiner the sixteen (16) references listed in the attached Substitute for Form PTO-1449 (Information Disclosure Statement by Applicant).

Fifteen of these references were cited in prior related U.S. patent application Serial Number 09/803,524 filed March 8, 2001 to which this application claims priority. Copies of these references have not been supplied herein since they were previously submitted in the parent case. One new U.S. Patent has been cited in this patent application. A copy of this reference has not been included

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully Submitted,


Kim Kanzaki
Attorney for Applicant
Reg. No. 37,652

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Complete if Known		
			Application / Conf. No.	Unknown /	
			Filing Date	March 19, 2004	
			First Named Inventor	Eric J. Crabill	
			Art Unit	Unknown	
Examiner Name			Unknown		
Sheet	1	of	2	Attorney Docket Number X-841-1C US	

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
		US- 5,574,927	11-12-96	Scantlin	
		US- 5,721,855	02-24-98	Hinton et al.	
		US- 5,796,981	08-18-98	Abudayyeh et al.	
		US- 5,799,169	08-25-98	Kalapathy	
		US- 5,802,359	09-01-98	Webb et al.	
		US- 5,832,251	11-03-98	Takahashi	
		US- 5,968,161 ²	10-19-99	Southgate	
		US- 6,081,888	06-27-00	Bell et al.	
		US- 6,134,707	10-17-00	Herrmann et al.	
		US- 6,272,453	08-07-01	Hoerig et al.	
		US- 6,341,345	01-22-02	Auslander et al.	
		US- 6,438,679	08-20-02	Sollars	
		US- 6,467,009 B1	10-15-02	Winegarden et al.	
		US-			
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FOREIGN PATENT DOCUMENTS						
Examiner Initials *	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³	Number ⁴ - Kind Code ⁵ (if known)			

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, P.O. Box 1450, Alexandria, Virginia, 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia, 22313-1450.

Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application / Conf. No.	Unknown /
				Filing Date	March 19, 2004
				First Named Inventor	Eric J. Crabill
				Art Unit	Unknown
				Examiner Name	Unknown
Sheet	2	of	2	Attorney Docket Number	X-841-1C US

OTHER -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Xilinx, "The Programmable Logic Data Book 2000", Chapter 3, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124.	
		Davidson, J.; "FPGA Implementation of a Reconfigurable Microprocessor"; Proceedings of the IEEE Custom Integrated Circuits Conference; May 9-12, 1993; pp. 3.2.1 thru 3.2.4.	
		Wirthlin et al.; "The Nano Processor: A Low Resource Reconfigurable Processor"; Proceedings of the IEEE Workshop on FPGAs for Custom Machines; April 10-13, 1994; pp. 23-30.	

Examiner Signature		Date Considered	
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¹ Applicant's unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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